



843.41648VX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. MIYAMOTO A et al
Serial No.: 10/765,916
Filed: January 29, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD OF MANUFACTURING THE SAME
Group: 2826
Examiner: V. MANDALA

AMENDMENT

MS: AMENDMENTS

April 11, 2005

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated December 10, 2004, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendment to the Title;

Amendments to the Claims; and

Remarks are included following the Amendment.

AMENDMENT TO THE TITLE

Please amend the title to read as follows:

METHOD OF MANUFACTURING A SEMICONDUCTOR INTEGRATED CIRCUIT
~~DEVICE AND METHOD OF MANUFACTURING THE SAME~~